

Xilinx FPGA Design Kit

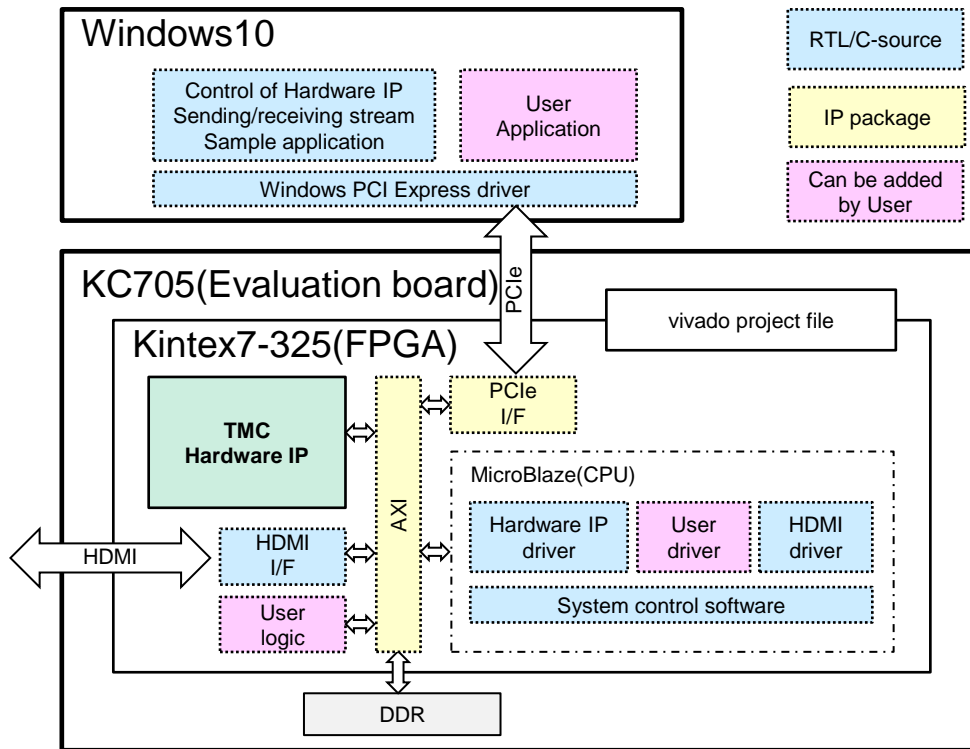
Powered by original computing algorithm "DMNA" based on mathematical methods

TMC has developed a design kit for FPGA to help customers implement TMC's Hardware IPs'. TMC provides the following reference designs.

- Vivado project file ➡ Easy to customize FPGA
- Sample design(RTL) ➡ Reduce development time for IP peripheral design
- Sample driver(Source) ➡ Reduce development time for IP integration
- Sample application(Source) ➡ Reduce development time for system design

You can customize sample data to adapt them for your own designs.

1 Design Kit Diagram



2 Supported hardware IP

- JPEG Encoder/Decoder(4pixel/clock)
- H.264/AVC Intraframe Encoder/Decoder

Note Specifications are subject to change without notice

Contact Information

7F, Gotanda NN Bldg., 2-12-19, Nishi-gotanda, Shinagawa-ku, Tokyo 141-0031

Techno Mathematical Co., Ltd.

TEL: +81-3-3492-3633

Email: info-sales@tmath.co.jp

FAX: +81-3-3492-3631

URL: <https://www.tmath.co.jp>